

# L1\_Cal Int/Comm Team Toolbox

## August 1-5, 2005

<b>Mon, Aug 1</b>	TAB→GAB (Mike/Cory) Studying TAB firmware to sync. SW→global TAB→L2 (Mikolaj) Ready for ORC to run long term. TAB→L3 (Lyn/Mark) Exercised dnld. Disabling ch1-3, gen. Sim. Data. ADF→TAB (Rahmi) Parity test this week. Pseudo random data. ADF ½ loaded. TRAN SYS→(Alan) Addressing LVDS routing issues. ORC (Lyn) Charlotte's work complete. Sch. ORC w/ Wolff, Walter. TAB→Cal_Track (Cory, KJ) Scheduled for Wed.
Alan Bagby Cory Mark Mike	
<b>Tues, Aug 2</b>	TAB→GAB (Mike/Cory) Studying TAB firmware to sync. SW→global TAB→L2 (Mikolaj) Splitter and fibers in house. TAB→L3 (Lyn/Mark) Mark working on VRB firmware. ADF→TAB (Rahmi) Parity test this week. Pseudo random data. ADF ½ loaded. TRAN SYS→(Alan) Strain relieved cables for now. Need to find BLS connectors. ORC (Lyn) Try to schedule ORC this week. TAB→Cal_Track (Cory, KJ) TAB not broken. ESD training requested. Hardware requested: magnetic lights, magnetic screwdrivers, wire cutters.
Alan Bagby Cory Dan Hal Marvin Mike	
<b>Wed, Aug 3</b>	TAB→Cal_Track (Cory/KJ) Work starts today. TAB→GAB (Mike) Signal tap studies. ADF→TAB→L2/L3 (Dan/Mike/Sabine) Sending pulser data through sys. ORC(Lyn) Contacted Wolff. Walt will be at D0 on Monday, Aug. 8
Alan Dan Marvin Cory Jorge	Sabine
<b>Thurs, Aug 4</b>	TAB→Cal_Track (Cory/KJ) Non-Zero data / sync gap recognized. Cont. today w/ known pattern data—latency meas. TAB firmware (Mike) Jaro working on solution to data corruption. ADF→TAB→L2/L3 (Dan/Mike/Sabine/Nayeem) Sent pulser data through RunIIa and RunIIb L1_Cal w/ BC10 trig. Data corresponds on selected TT. Marvin/Alan suggested several studies that can be done using Cal pulser. TRAN SYS (Alan) Notice: Only install ACT cards that have front panels into an ADF crate. ORC(Lyn) ADF write up submitted by Edmunds. L1_Cal review date set August 26-27. Hal preparing talk.
Alan Cory Hal Jorge Lyn Mark Marvin Mike Sabine	
<b>Fri, Aug 5</b>	TAB→Cal_Track (Cory/KJ) # of events doesn't scale w/ rate. Using diag. tools to resolve issue. TAB firmware (Mike) Jaro working on solution to data corruption from SW to global chip. Setting timing between TAB→Cal_Track,GAB. ADF→TAB→L2/L3 (Dan/Mike/Sabine/Nayeem) Confirming pulser software is ready w/Peter. Scheduling a Cal pulser run for next window. ADF→TAB(Rahmi) Ran parity test. Central region ok, overlap region problem. TAB→L3 BER(Lyn/Mark) Trying new firmware today.
Cory Jorge Lyn Mark Marvin Mike Nayeem Rahmi Sabine	